U.S. UTILITY Patent Application

PATENT NUMBER and ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10032761	12/27/2001	324	765	~~2858~~ 2829	Jimmy - V

**APPLICANTS: Takechi Keizo; Ohsaki Akio; Hayashi Yoshihiko; Murata Kazuhiko;

**CONTINUING DATA VERIFIED:

None

** FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-150853 05/21/2001

PG-PUB	DO NOT PUBLISH ☐	RESCIND ☐	
Foreign priority claimed	☒ yes ☐ no		ATTORNEY DOCKET NO
35 USC 119 conditions met	☒ yes ☐ no		16869N-039000US
Verified and Acknowledged Examiners's intials			

TITLE : Driver circuit integrated with load current output circuit, pin electronics and IC tester having thereof

U.S.DEPT. OF COMM./PAT.& TM-PTO-436L(Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE			DRAWING	
Amount Due	Date Paid		Sheets Drwg. / Figs.Drwg. / Print Fig.	
		Primary Examiner		
☐ TERMINAL DISCLAMER		PREPARED FOR ISSUE	Application Examiner	

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